Seat No.:	Enrolment No.

## GUJARAT TECHNOLOGICAL UNIVERSITY

		BE - SEMESTER-IV(NEW) EXAMINATION - WINTER 2022	,
Sub	viect	Code:3140702 Date:13-12	
	•		2-2022
Subject Name: Operating System			0
Time:10:30 AM TO 01:00 PM Total Marks:		rks:70	
Insti	ruction		
		Attempt all questions.	
		Make suitable assumptions wherever necessary.	
		Figures to the right indicate full marks. Simple and non-programmable scientific calculators are allowed.	
	7.	Simple and non-programmable scientific calculators are anowed.	MARKS
<b>Q.</b> 1	<b>l</b> (a)	Differentiate multiprocessing and multiprogramming operating system.	03
	<b>(b)</b>	1 0 1	04
	(c)	What is process? Explain process control block with all parameters.	07
Q.2	2 (a)	Differentiate user level and kernel level thread.	03
	<b>(b)</b>	What is scheduling? Explain the types of schedulers.	04
	(c)	List out various criteria for good process scheduling algorithms.	07
		Illustrate non-preemptive priority scheduling algorithm. <b>OR</b>	
	(c)	Differentiate process and thread. Explain process state diagram.	07
Q.3	3 (a)	Define following terms:	03
ν	, ( <b>u</b> )	(i)Critical section (ii) Mutual exclusion (iii) Bounded waiting	00
	<b>(b)</b>		04
	(c)		07
	(0)	OR	0.
Q.3	3 (a)	Explain Resource allocation graph.	03
	( <b>b</b> )	1 5 1	04
	(c)	Explain the Banker's algorithm for deadlock avoidance with an	07
	(-)	example.	
<b>Q.</b> 4	<b>1</b> (a)	Explain segmentation.	03
ν.	(b)		04
	()	fragmentation.	
	(c)	Explain paging hardware with TLB.	07
	(-)	OR	
<b>Q.</b> 4	<b>1</b> (a)	Explain address binding.	03
	$(\mathbf{b})$		04
	(c)	What is page fault? Explain steps to handle page fault with diagram.	07
	( )		
Q.5	5 (a)	Discuss the major goals of I/O software.	03
~··	(b)		04
	(6)		<b>.</b> .
	(c)	Draw the block diagram for DMA. Write steps for DMA data transfer.  OR	07
Q.5	5 (a)		03
Z	(b)		04
	(c)	Write short note on RAID levels.	07

\*\*\*\*\*