Seat No.:	Enrolment No.

GUJARAT TECHNOLOGICAL UNIVERSITY

BE- SEMESTER-IV (NEW) EXAMINATION – WINTER 2020

Subject Code:3140915 Date:26/02/2021

Subject Name:Power Electronics

Time:02:30 PM TO 04:30 PM Total Marks:56

Instructions:

- 1. Attempt any FOUR questions out of EIGHT questions.
- 2. Make suitable assumptions wherever necessary.
- 3. Figures to the right indicate full marks.

			Marks
Q.1	(a)	Draw waveform of full bridge topology of 1-Phase bridge inverter with resistive load.	03
	(b) (c)	Discuss SVPWM technique in brief. Sketch unipolar and bipolar PWM	04 07
Q.2	(a) (b)	Derive output voltage equation for signle phase full wave rectifier State the merits & demerits of current source inverter & voltage source inverter.	03 04
	(c)	Explain 3 phase inverter operation for 120 with the gate voltage and phase voltage waveform	07
Q.3	(a) (b)	Derive output voltage equation for signle phase half wave rectifier. Give classification of different techniques for voltage control of inverter. Explain any one.	03 04
	(c)	Analysis of working of 3-φ half wave controlled rectifier with RL load with continuous conduction mode	07
Q.4	(a) (b) (c)	Derive AC voltage controller average output voltage equation Explain protection of SCR and its design. Explain Dual Converter.	03 04 07
Q.5	(a) (b) (c)	Explain CSI Explain Snubber circuit and its design Explain the basic principle of operation of a cycloconverter with neat equivalent circuit diagram.	03 04 07
Q.6	(a) (b) (c)	Explain any one chopper. Derive inveter output voltage Explain Buck Boost converter	03 04 07
Q.7	(a) (b) (c)	Explain Matrix converter Define Mid point CycloConvereter. Explain DC to DC converter.	03 04 07
Q.8	(a) (b) (c)	Give Application of Power Electronics. Explain Buck converter. Explain three stage sequence control AC voltage controller with RL load	03 04 07
