

REGISTRATION FORM

One Week
Short Term Training Programme on
“DIGITAL IMAGE AND
SIGNAL PROCESSING”
19TH - 24TH NOVEMBER 2016

Name: _____

Department: _____

Institution: _____

Address: _____

Cell Phone: _____

E-mail: _____

Category: Student / Faculty

I am enclosing a demand draft in favor of “G H Patel College of Engineering and Technology” payable at Vallabh Vidyanagar as Registration Fee ₹ _____.

Demand Draft No.: _____

Drawee Bank: _____

Link for Registration: <https://goo.gl/AaKc7H>

Signature of
Participant

Signature of Head
of Institution with seal

REGISTRATION DETAILS

The registration fee is as follows:

For PG Students / Research Scholars: ₹ 1000 /-

For Faculties: ₹ 1500 /-

The course fee includes Registration kit, High tea, Snacks, Lunch and Certificate. The programme consists of interactive expert talks & hands on laboratory sessions.

WHO CAN APPLY?

PG students, Research Scholars & Faculties working in this domain.

LAST DATE OF REGISTRATION: - 10th Nov. 2016

INTIMATION TO PARTICIPANTS: 12th Nov 2016

STTP CONVENERS

Prof. Kavindra Jain and Prof. Arpan Desai

Asst. Professor,

Electronics & Communication Engineering

Department , GCET

Mobile: 9904467724

9909980477

Email: kavindrajain@gcet.ac.in

arpandesai@gcet.ac.in

KEYNOTE SPEAKERS

- **Dr. Arnav Bhavsar** IIT Mandi
- **Dr. Nagendra Gajjar** Nirma University
- **Dr. Nitin George** IIT Gandhinagar
- **Dr. Manjunath Joshi** DA-IICT
- **Dr. Chirag Paunwala** SCET Surat
- **Dr. Aditya Tatu** DA-IICT
- **Dr. Vishvjit Thakar** ADIT

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Organized by

**Electronics & Communication
Engineering Department**

[Accredited by NBA]



G H Patel College of Engineering & Technology

Vallabh Vidyanagar – 388120, Gujarat, India

(A Charutar Vidya Mandal Institution)

Tel.: (02692) 231651; Fax: (02692) 236896

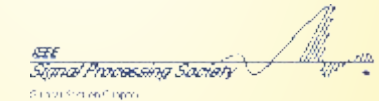
Website: www.gcet.ac.in

Sponsored and Technically Supported by



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Dr. C. L. Patel
Chairman, CVM



PATRON

Dr. Himanshu B. Soni
Principal, GCET

MENTOR

Dr. Bhaskar V. Thakkar
HEAD, EC Department

ABOUT THE STTP

Digital Image and Signal Processing (DISP) has emerged as a multimedia technological revolution. The objective of this workshop is to provide an insight of applications affiliated in the domains of digital signal and image processing. Exploration and visualization using a subsection of MATLAB will extend a workhorse of choice for many computational intensive applications. Intelligent systems are expected to become an integral part of our daily lives. Modern applications of DISP will provide a diversified platform for various domains of development.

The workshop is aiming towards key areas like **3D Computer Vision, Medical Image Analysis, Machine Learning and its application, Wavelet Transforms, Basics of filters, Optimum Filters, Development and Research Trends in Image and Signal Processing, DSP Applications on power electronics and renewable energy converters.**

ABOUT THE MANAGEMENT

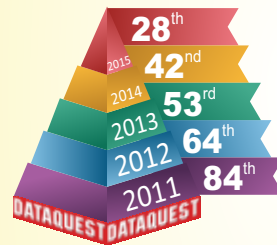


Vidyanagar established by CVM in 2000.

GCET is managed by Charutar Vidya Mandal, CVM, a registered charitable trust established in 1945. Today CVM manages 48 educational institutions in Vallabh Vidyanagar and its satellite township New Vallabh

ABOUT THE INSTITUTE

To meet the needs of industries in Gujarat and beyond, to contribute to the growth of industries through modern Information Technology, and to provide talented youngsters of Gujarat with engineering education of a high standard, Charutar Vidya Mandal (CVM) decided in 1995 to set up a private engineering college as per the norms of AICTE and the policy of the State Government. Finally in 1996, CVM started G H Patel College of Engineering & Technology (GCET) located at Vallabh Vidyanagar, Gujarat. Currently the institute is running with eight disciplines at graduate level and seven at post graduate level.



- GCET is ranked 28th among top 100 engineering colleges across the country as surveyed by Data Quest 2016.
- GCET is awarded as 'Outstanding institution in western region' by ABP News in 2015 and 2016.

ABOUT DEPARTMENT

G H Patel College of Engineering & Technology (GCET) has started the Undergraduate Department of Electronics & Communications in 1997. The Department has excellent laboratory facilities both for teaching and research. The Department organizes workshops, seminars and runs summer /winter schools for the benefit of engineering college teachers and professional engineers.

GCET has started Post Graduate courses in the Communication Engineering in the year 2007-08 and Embedded Systems from Academic Year 2013-14.

IEEE SPS GUJARAT SECTION CHAPTER

Established on June 19, 2013 the Signal Processing Society (SPS) Chapter is one of the most vibrant Chapters of the IEEE Gujarat Section. With a whopping 149 members, the chapter is the third largest chapter of IEEE Gujarat section after the Industry Applications Society (IAS) chapter and the Power and Energy Society (PES) chapter. The chapter is supporting the activities such as workshops, short term training programs, expert talks, seminars etc. organized by various institutes throughout the state. As a result, it received the Chapter Certification Award (Among Top 20 Chapters) in 2014 and was honored with the 2015 Chapter of the Year Award among all SPS chapters of the world during ICASSP-2016 at Shanghai, China.

ORGANIZING COMMITTEE

Prof. Rohit Parmar and Prof. Parthesh Mankodi

Asst. Professor,

Electronics & Communication Engineering
Department , GCET

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