

Registration Details:

Registration can be done by filling the registration form in the prescribed format along with the registration fees (also available on www.bvmengineering.ac.in)

Registration Fees:

Faculty Members & Research scholars of academic institutions/ Students: Rs. 1000/-
Delegates from industries: Rs. 2000/-

The registration fees should be paid in form of Cash or Bank Demand Draft drawn in favour of **Principal, BVM Engineering College**, payable at **Vallabh Vidyanagar**. The duly filled registration form and the Demand Draft should be sent to the following address so as to reach on or before 22nd December, 2016.

The Coordinator,

“Designing Embedded Systems and Internet of Things”

Department of Electronics Engineering

BVM Engineering college, Vallabh Vidyanagar,
Anand-388120 (Gujarat)

Contact Numbers: (02692) 230104

Website: www.bvmengineering.ac.in

Email: desiot.sttpbvmel@bvmengineering.ac.in

The registration fees include course material, participation kit, lunch and tea. No TA/DA will be paid. Accommodation will be provided on first come first served basis in the guest house of BVM College on chargeable basis.

Last Date for receiving Application:

22th, December, 2016.

Confirmation of Participation:

23th, December, 2016 (by e-mail)

Chief Patron

Dr. C. L. Patel

(Hon. Chairman, CVM)

Patrons

Dr. S. G. Patel (Hon. Secretary, CVM)

Er. V. M. Patel (Hon. Jt. Secretary, CVM)

Shri. B. P. Patel (Hon. Jt. Secretary, CVM)

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Dr. Indrajit Patel

(Principal, BVM Engineering College)

TEQIP- II

Dr. D. G. Thakore (TEQIP-II Coordinator),

Dr. P. M. George, Dr. T. D. Pawar, Dr. S. D. Dhiman

Prof. P. B. Swadas, Prof. S. B. Patel, Dr. H. P.

Patolia, Dr. M. M. Vegad, Dr. D. M. Patel

Advisors

Prof. Usha Neelkanthan (LDEC, Ahmedabad)

Dr. K. R. Parmar (Consultant, eiTRA)

Dr. R. A. Thakkar (VGEC, Chandkheda)

Dr. C. H. Vithalani (GEC, Rajkot)

Dr. V. K. Thakar (ADIT, New V.V.Nagar)

Dr. B. V. Thakkar (GCET, V.V.Nagar)

Dr. Nagendra Gajjar (Nirma Uni., Ahmedabad)

Mr. Sukanta Saha (SDE, BSNL, Anand)

Dr. N. J. Kothari (DDU Uni., Nadiad)

TEQIP-II Sponsored

One Week

Short Term Training Program on

“Designing Embedded Systems and Internet of Things”

December 26th-30th, 2016



Organized by

Department of Electronics Engineering

Birla Vishvakarma Mahavidyalaya Engineering College

(A Charutar Vidyamandal Institution)

Vallabh Vidyanagar, Anand. Gujarat-388120



CHARUTAR VIDYAMANDAL
VALLABH VIDYANAGAR, GUJARAT, INDIA

Convener

Dr. T. D. Pawar

(Professor & Head)

Program Coordinators

Dr. D. M. Patel Dr. J. M. Rathod

(9824389662) (9428902849)

Program Co-coordinators

Prof. D. L. Vala, Prof. M. S. Holia

Prof. P. H. Panchal, Prof. C. S. Patel

Team Members

Prof. A. A. Daiya, Prof. K. D. Patel, Prof. M. M.

Khambalkar, Prof. M. M. Solanki, Prof. M.P.

Prajapati, Prof. C. J. Jayswal, Prof. N. R. Ada,

Prof. A. N. Bhatt

About CVM

One of the greatest sons of India Late Sardar Vallabhbhai Patel inspired Shri Bhaikaka and Shri Bhikabhai Saheb for rural resurgence of post-independent India through education, and thus Charutar Vidya Mandal was established in the year 1945 as a charitable trust with a prime objective of rural development through education to bring about the social awakening, social upliftment and enrichment. Over the subsequent years, Dr. H M Patel consolidated the efforts put in by the founders. In 1994 Dr. C L Patel took over the reigns of CVM as the Chairman, and through his dynamic leadership, missionary zeal and visionary outlook he not only rejuvenated the ongoing education system but added a modern education campus at New Vallabh Vidyanagar. CVM is one of the major education trust of our country catering quality education to all sections of the society.

About BVM

BVM is a Premier Grant-in-aid Autonomous Institution of Charutar Vidya Mandal (CVM) founded in 1948. Motto of BVM: Work is Worship. BVM is the first degree engineering college of the Gujarat state. More than 18000 engineers have graduated from this college. BVM offers B.E., M.E./M. Tech. and Ph.D. degree Program. The College is affiliated to the Gujarat Technological University & is situated at Vallabh Vidyanagar, which is about 4 km from the Anand town. The most striking features of BVM is the dedicated and experienced faculty base. BVM has been granted academic autonomy by the UGC from A.Y: 2015-16.

About Electronics Department

The Departments of Electronics Engineering was established in 1984 at BVM. Dedicated faculty members, meritorious students, well equipped laboratories, and skilled support staff, are major strengths of the department.

About TEQIP-II

TEQIP-II aims to improve the quality of Technical Education by providing inputs like modernization of

laboratories, workshops, library, faculty development, networking between institutions, curriculum development, research and improve Interaction with Industries etc., to become dynamic, demand driven, quality conscious, responsive to rapid technological developments occurring both at national and International levels.

About the Course

Embedded Systems are extensively used for designing of electronic systems because of its low cost and portability. Designing embedded systems with Internet of Things is to optimize the systems in terms of data storage and internet connectivity. The objective of the program is to provide an opportunity to faculty members for updating the latest emerging developments in designing embedded systems and Internet of Things.

Course Contents

The focus of this short term course is on learning the designing concepts of embedded systems with various embedded controllers. The training program is designed to give exposure on various development tools for embedded controller such as Keil, Xilinx Vivado for FPGA based design, ARM development tool, Embedded application based on Raspberry Pi embedded development board.

Who should attend?

Faculties of academic institutions of circuit branches (EL/EC/EE/CP/IT/ETC) of Engineering, and professionals working in the area of Electrical, Electronics, Computer, Information Engineering or any other relevant branch.

Resource Persons

The course will be conducted through experienced faculty members from reputed engineering colleges, and experts from industries.

Last Date for Receipt of Application, 22th Dec-2016

TEQIP-II Sponsored

Short Term Training Program on

“Designing Embedded Systems and Internet of Things”

December 26th-30th, 2016

Registration Form

Name: _____

Designation: _____

Institute/ Organization: _____

Experience in years: _____ (Teaching),
_____ (other)

Address: _____

Contact No. : _____

E-mail: _____

Registration Fee Payment Details:

Bank & Branch: _____

DD No. : _____ Amt (Rs.) : _____

Accommodation Required Yes/No: _____

Certified that above information is true to the best of my knowledge and the applicant is authorized to attend.

Signature of Applicant

Signature & Seal
of
Employer/Head
of Institute